### PATENT APPLICATION

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q60938

Takashi ABE

Appln. No.: 09/685,771

**RECEIVED** 

Group Art Unit: 2827

Confirmation No.: 5049

JAN 3 1 2003

Examiner: I. Patel

Filed: October 11, 2000

OFFICE OF PETITIONS

For:

**CIRCUIT BOARD** 

STATEMENT UNDER 37 C.F.R. § 1.704(d)

Commissioner for Patents Washington, D.C. 20231

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed concurrently herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and that the communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of said Information Disclosure Statement.

Respectfully submitted,

Howard L. Bernstein

Registration No. 25,665

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WASHINGTON OFFICE

23373

PATENT TRADEMARK OFFICE

Date: January 31, 2002

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## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

- 1. Japanese Unexamined Patent Publication No. 7-263871, published October 13, 1995.
- 2. Japanese Unexamined Patent Publication No. 11-220263, published August 10, 1999.
- 3. Japanese Unexamined Patent Publication No. 4-261098, published September 17, 1992.

One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date for an application other than a continued prosecution application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the



U.S. Appln. No.: 09/685,771

merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The present Information Disclosure Statement is being filed thirty days or fewer from the communication from a foreign patent office and a Statement Under 37 C.F.R. §1.704(d) is attached.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses here with a copy of a corresponding Japanese Office Action dated January 21, 2003 and an English translation of the pertinent portions thereof which cites such documents and indicates the degree of relevance found by the foreign office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,

Howard L. Bernstein

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PATENT TRADEMARK OFFICE

Date: January 31, 2003



of



Application Number Confirmation Number

Attorney Docket Number

Filing Date

Complete if Known

5049

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October 11, 2000

Substitute for Form 1449 A & B/PTO

Sheet

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

	First Named Inventor	Takasiii Ai
use as many sheets as necessary)	Art Unit	2827
	Examiner Name	I. Patel

			U.S. P	ATENT DOCUME	NTS
Examiner Initials*	Cite No.1	Document 1	Document Number		
		Number	Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documen
		US			
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		US			OFFICE OF PETITIONS

	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document			Publication Date	Name of Patentee or	Translation <sup>6</sup>
		Country Code <sup>3</sup>	Number⁴	Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document	Translation
		JP	7-263871		10-13-1995		No
		JP	11-220263		08-10-1999		No
		JP	4-261098		09-17-1992		No
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	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
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		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS  Cite No. I Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.

Examiner Signature	Date Considered

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the part document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to indicate here if English language Translation is attached.

### Ref. Q60938

Claims: 1 through 6

• Cited Literature: 1 through 3

Remarks:

Cited Literature 1 through 3 describe a circuit substrate equipped with a ground plane on the obverse and the reverse where electronic components are implemented.

In addition, Cited Literature 1 describes a shorting plane on the edge of the substrate that short-circuits the ground layer.

#### List of Cited Literature

- 1. Japanese Unexamined Patent Application Publication H7-263871
- 2. Japanese Unexamined Patent Application Publication H11-220263
- 3. Japanese Unexamined Patent Application Publication H4-261098